

Brian M. Krzanich

Vice President

General Manager, Manufacturing and Supply Chain

INTEL CORPORATION

Brian M. Krzanich is vice president and general manager of Manufacturing and Supply Chain for Intel Corporation. He is responsible for all aspects of Intel's factories and operations worldwide.

Previously, Krzanich was responsible for Assembly Test from 2003 - 2007. From 2001-2003, he was responsible for the implementation of the 0.13-micron logic process technology across Intel's global factory network. From 1997 to 2001, Krzanich served as the Fab 17 plant manager, where he oversaw integrating Digital Equipment Corporation's semiconductor manufacturing operations into Intel's manufacturing network. The assignment included building updated facilities as well as initiating and ramping 0.18-micron and 0.13-micron process technologies. From 1996 to 1997, Krzanich was the Fab 6 plant manager in Arizona. From 1994 to 1996, he was a manufacturing manager for Fab 12 in Arizona. He also served as a process engineer at various Intel locations. Krzanich joined Intel in 1982.

Krzanich was presented an Intel Achievement Award in 1999. He holds one patent for semiconductor processing.

Krzanich received a bachelor's degree in Chemistry from San Jose State University in 1982.